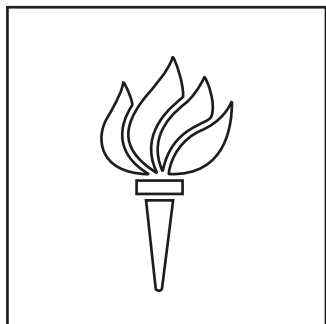


NYU Ability Project

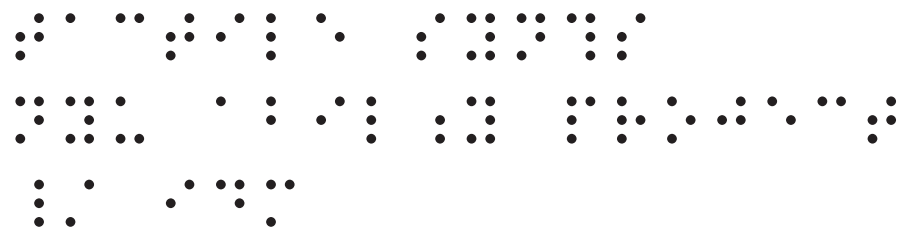
Serge Modular 73-75 Panels



NYU

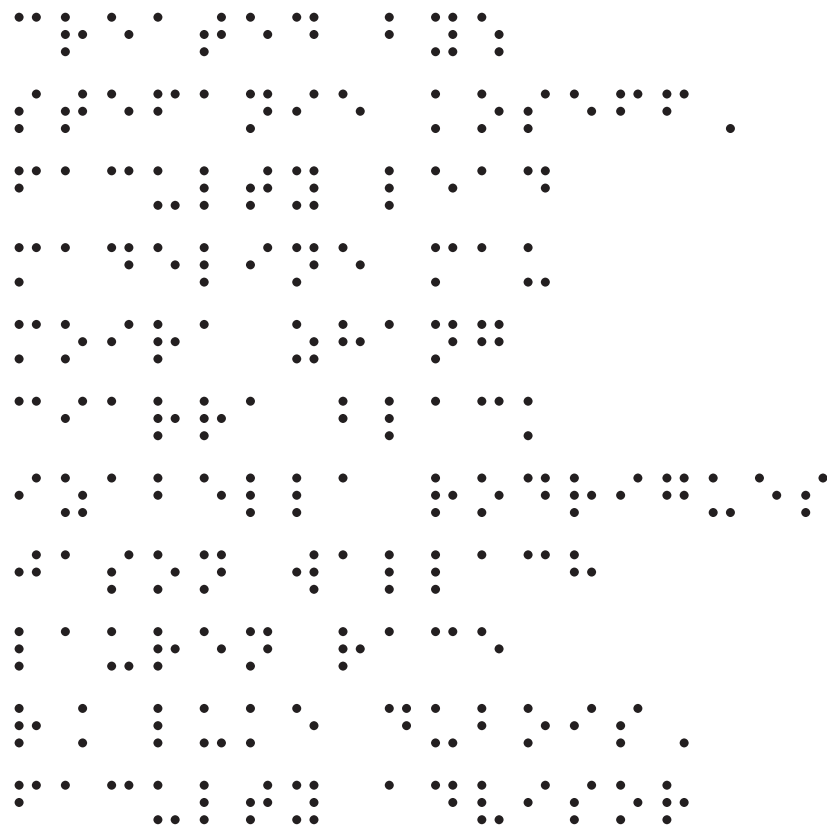
Ability
Project

NYU Ability Project



Tactile Synths

NYU Ability Project / IDM



Created by:

Stefanie Koseff, Faculty Lead

Madeline Mau

Moirra Zhang

Ciarra Black

Izabella Rodrigues

Jason Wallach

Lauren Race

R. Luke DuBois, Faculty Advisor

panel guide






VOICE

























CONTROL

























HOMEBUILT #1

























HOMEBUILT #2

























PRESET




OSC




OSC




TWS




VCF




VCA




VCA








RING

ENV




ADP




NOI




POS




NEG




COM




PRC




SSG




OSC




TWS




P&T




COM




PRC




VCA








RING

RVB




PRE




POS




NEG




ENV




ENV




ENV




ADP




SEQ



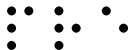
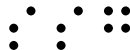
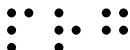
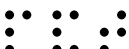
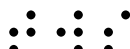
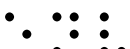
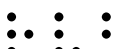

PRG




TBS



table of contents

 KEY	 1	 NOI	 5	 PRE	 10	 SSG	 16
 ADP	 2	 OSC	 6	 PRG	 11	 TBS	 17
 COM	 2	 P&T	 7	 RING	 14	 TWS	 18
 ENV	 3	 POS	 8	 RVB	 14	 VCA	 19
 NEG	 4	 PRC	 9	 SEQ	 15	 VCF	 19

KEY

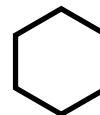
symbol key



ac in



misc in



trs in



ac out



misc out



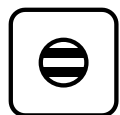
trs out



dc in



basic knob



dc out



offset (+) knob



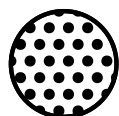
pulse in



scale (x) knob



pulse out



button

ADP

adapter



a out



a in



b out



b in



c out



c in

COM

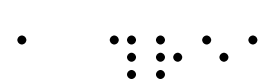
triple comparator



a in 1



a out



a thresh



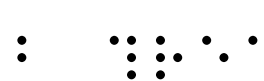
a in 2



b in 1



b out



b thresh



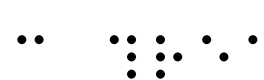
b in 2



c in 1



c out



c thresh



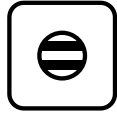
c in 2

ENV

envelope generator



end



out



window



start in



out



window +



cycle



hold



window vc



duration vc



rise



fall



duration x

NOI

noise source



white



white



pink



pink



s/h ac



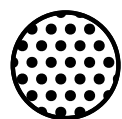
s/h dc



stepped ac



stepped dc



manual



trigger

OSC

oscillator



sine ac



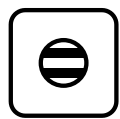
ws x



sine dc



waveshape



saw dc



saw ac



fm



fm



fm x



fm x



sync



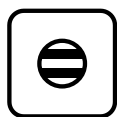
frequency

⠠⠠⠠⠠⠠ P&T

⠠⠠⠠ ⠠⠠⠠ ⠠⠠⠠⠠⠠⠠ peak & trough



⠠⠠⠠ ⠠⠠⠠
peak out



⠠⠠⠠⠠⠠⠠ ⠠⠠⠠
trough out



⠠⠠⠠ ⠠⠠⠠
peak out



⠠⠠⠠⠠⠠⠠ ⠠⠠⠠
trough out



⠠⠠⠠ ⠠⠠⠠ ⠠⠠
peak in



⠠⠠⠠⠠⠠⠠ ⠠⠠⠠ ⠠⠠
trough in



⠠⠠⠠ ⠠⠠⠠ ⠠⠠
peak in



⠠⠠⠠⠠⠠⠠ ⠠⠠⠠ ⠠⠠
trough in



⠠⠠⠠ ⠠⠠⠠ ⠠⠠
peak in



⠠⠠⠠⠠⠠⠠ ⠠⠠⠠ ⠠⠠
trough in



⠠⠠⠠ ⠠⠠⠠ ⠠⠠
peak in



⠠⠠⠠⠠⠠⠠ ⠠⠠⠠ ⠠⠠
trough in

POS

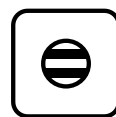
positive slew



a vc



a in



a out



a x



a start



a high



a +



a sustain



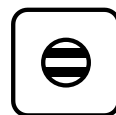
a end



b vc



b in



b out



b x



b start



b high



b +



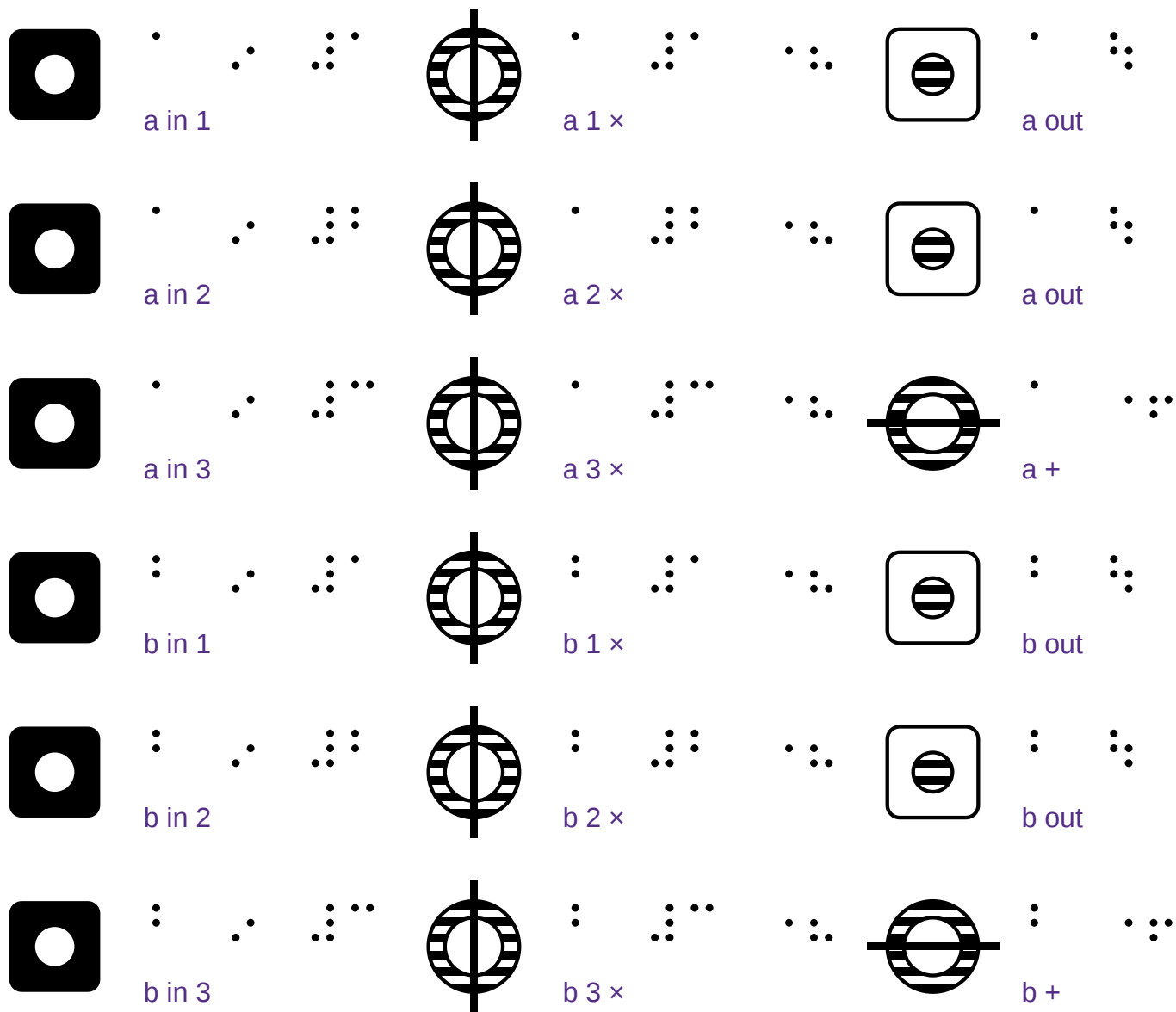
b sustain



b end

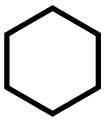
PRC

dual processor



PRE

preamplifier



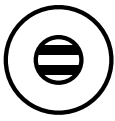
input



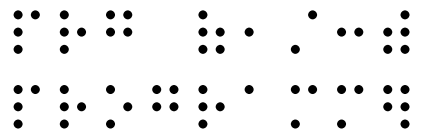
gain



out



out



PRG (1/3) programmer



1 out



2 out



3 out



4 out



1 a



2 a



3 a



4 a



1 b



2 b



3 b



4 b



1 c



2 c



3 c



4 c



1 in



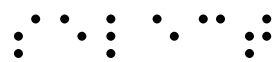
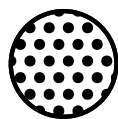
2 in



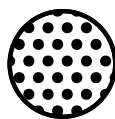
3 in



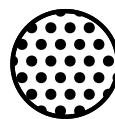
4 in



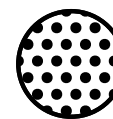
select



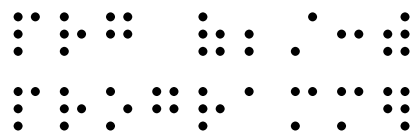
select



select



select



PRG (2/3) programmer



5 out



6 out



7 out



8 out



5 a



6 a



7 a



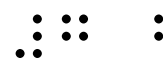
8 a



5 b



6 b



7 b



8 b



5 c



6 c



7 c



8 c



5 in



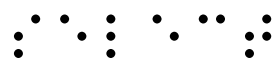
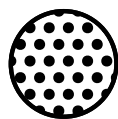
6 in



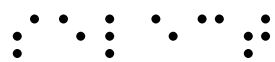
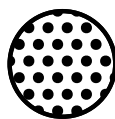
7 in



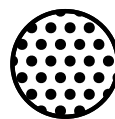
8 in



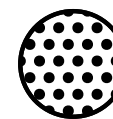
select



select



select



select

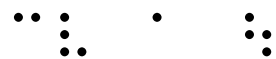
PRG (3/3)
programmer



all out



all out



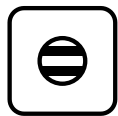
cv a out



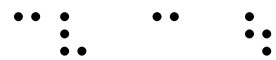
cv a out



cv b out



cv b out

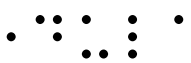
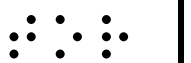


cv c out



cv c out

RING

ring modulator





out





y






vc y





x






vc x







x - xy

RVB





reverberator





out





out






mix






wet






wet





in

⋮ ⋮ ⋮ ⋮ **SEQ**

⋮ ⋮ ⋮ ⋮ ⋮ ⋮ ⋮ ⋮ sequencer



5 out



5 out



6 out



6 out



4 out



4 out



7 out



7 out



3 out



3 out



8 out



8 out



2 out



2 out



9 out



9 out



1 out



1 out



10 out



10 out



clock



hold



reset



step

SSG

smooth & stepped generator

smooth & stepped generator



smooth vc



smooth out



smooth x



cycle



smooth in



smooth +



hold



coupler



stepped vc



sample



coupler



stepped x



cycle



stepped in



stepped +



stepped out



bi-directional router



TWS

triple waveshaper



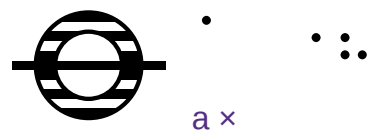
a vc1



a vc2



a out



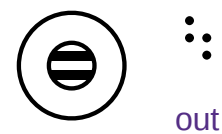
a x



a in



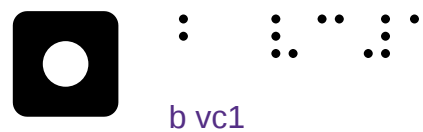
a out



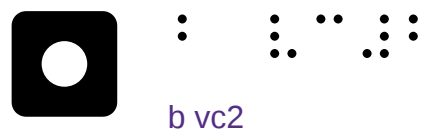
out



ac



b vc1



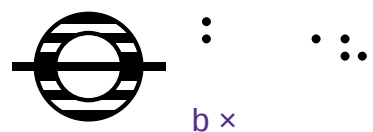
b vc2



b out



dc



b x



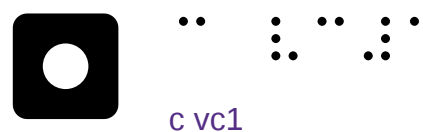
b in



b out



lin



c vc1



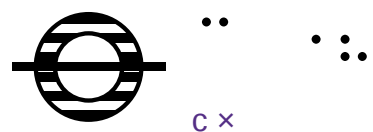
c vc2



c out



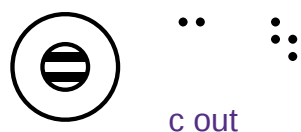
log



c x



c in



c out



gain

VCA gate

VCF filter

